

Ultra LowCapacitance ESD Protection Diode

DESCRIPTION

SLESD5311X is a low-capacitance Transient Voltage Suppressor (TVS) designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces. With typical capacitance of 0.5pF, SLESD5311X is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC 61000-4-2 (ESD), Level 4 ($\pm 15\text{kV}$ air, $\pm 8\text{kV}$ contact discharge), IEC 61000-4-4 (electrical fast transient - EFT) (40A, 5/50 ns), very fast charged device model (CDM) ESD and cable discharge event (CDE), etc.

SLESD5311X uses ultra-small DFN1006 package. Each SLESD5311X device can protect one high-speed data line. It offers system designers flexibility to protect single data line where space is a premium concern. The combined features of low capacitance, ultra-small size and high ESD robustness make SLESD5311X ideal for high-speed data port and high-frequency line applications, such as cellular phones and HD visual devices.

ORDERING INFORMATION

- ✧ Device: SLESD5311X
- ✧ Package: DFN1006
- ✧ Marking:
- ✧ Material: Halogen free
- ✧ Packing: Tape & Reel
- ✧ Quantity per reel: 10,000pcs

FEATURES

- ✧ Transient protection for high-speed data lines
 - IEC 61000-4-2 (ESD) $\pm 15\text{kV}$ (Air)
 - $\pm 8\text{kV}$ (Contact)
- IEC 61000-4-4 (EFT) 40A (5/50 ns)
- Cable Discharge Event (CDE)
- ✧ Package optimized for high-speed lines
- ✧ Ultra-small package (1.0mm $\times$ 0.6mm $\times$ 0.5mm)
- ✧ Protects one data, control line
- ✧ Low capacitance: 0.5pF (Typical)
- ✧ Low leakage current
- ✧ Low clamping voltage

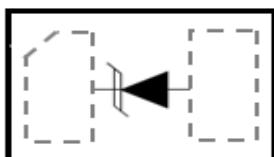
MACHANICAL DATA

- ✧ DFN1006 package
- ✧ Flammability Rating: UL 94V-0
- ✧ Packaging: Tape and Reel
- ✧ High temperature soldering guaranteed: $260^{\circ}\text{C}/10\text{s}$
- ✧ Reel size: 7 inch

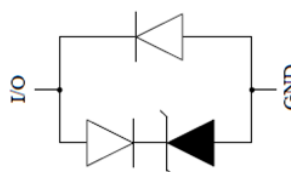
APPLICATIONS

- ✧ Serial ATA
- ✧ Desktops, Servers and Notebooks
- ✧ Cellular Phones
- ✧ MDDI Ports
- ✧ USB Data Line Protection
- ✧ Display Ports
- ✧ Digital Visual Interfaces (DVI)

PIN CONFIGURATION



CIRCUIT DIAGRAM



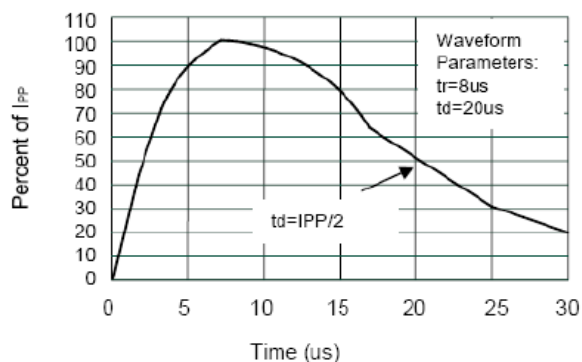
ABSOLUTE MAXIMUM RATING

Symbol	Parameter	Value	Units
V_{ESD}	ESD per IEC 61000-4-2 (Air)	± 20	kV
	ESD per IEC 61000-4-2 (Contact)	± 20	
P_{PP}	Peak Pulse Power (8/20 μ s)	60	W
T_{OPT}	Operating Temperature	-55~125	$^{\circ}$ C
T_{STG}	Storage Temperature	-55~150	$^{\circ}$ C

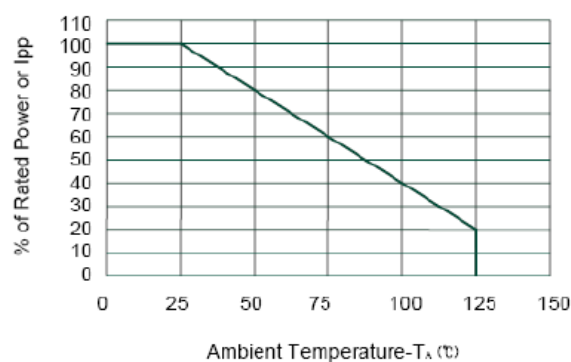
ELECTRICAL CHARACTERISTICS (Tamb=25 $^{\circ}$ C)

Symbol	Parameter	Test Condition	Min	Typ	Max	Units
V_{RWM}	Reverse Working Voltage				5.0	V
V_{BR}	Reverse Breakdown Voltage	$I_T = 1mA$	6.0			V
I_R	Reverse Leakage Current	$V_{RWM} = 5V$			100	nA
V_C	Clamping Voltage	$I_{PP} = 1A, t_p = 8/20\mu s$			10	V
		$I_{PP} = 4A, t_p = 8/20\mu s$			15	V
C_J	Junction Capacitance	$V_R = 0V, f = 1MHz$		0.5		pF

ELECTRICAL CHARACTERISTICS CURVE

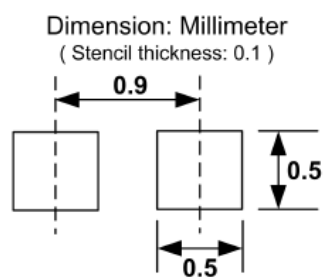
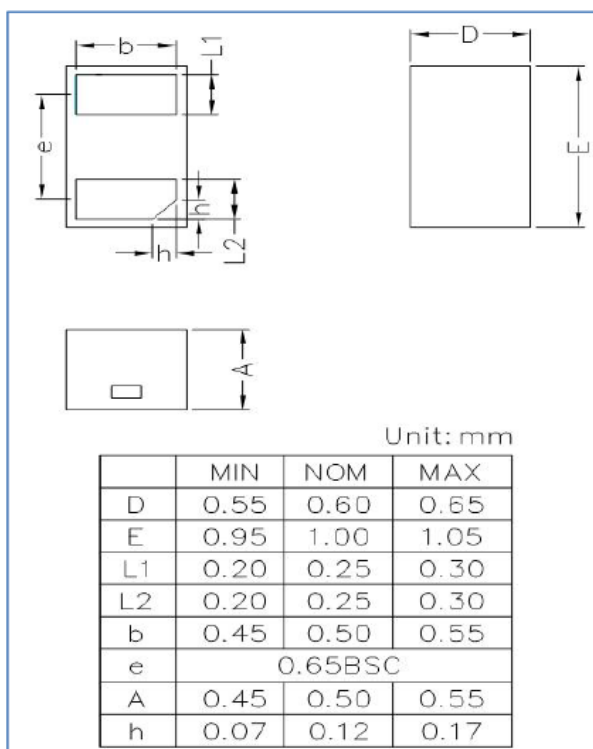


Pulse Waveform



Power Derating Curve

DFN1006 PACKAGE OUTLINE DIMENSIONS



Soldering Footprint